

AMENDMENT

To: Examiner of the Patent Office

1. Identification of the International Application

PCT/JP2003/007672

5 2. Applicant

Name: TOKYO ELECTRON DEVICE LIMITED

Address: 1, Higashikata-cho, Tsuzuki-ku, Yokohama-shi,
Kanagawa 224-0045, JAPAN

Country of nationality : Japan

10 Country of residence : Japan

3. Agent

Name : 9540 Patent Attorney KIMURA Mitsuru

Signature Kimura Mitsuru

15 Address : 2nd Floor Kyohan Buidlding, 7, kandanishiki-cho
2-chome, Chiyoda-ku, Tokyo 101-0054, Japan

4 . Item to be amended

Claims

5. Subject Matter of Amendment

20 (1) Page 42, line 4, the word " non-volatile" should be inserted after "a"
(first occurrence).

(2) Page 42, line 5, the expression ", each of said blocks including
physical pages, each of said physical pages including a logical page and a
redundancy portion " should be inserted after "are allocated".

25 (3) Page 42, line 6, the expression "(BPT)" should be inserted after "an
address translation table".

(4) Page 42, line 10, the expression "(BSI)" should be inserted after "a

write pointer”.

- (5) Page 42, line 12, the expression “when this memory system is activated, performs initializing process in which reads data from the redundancy portions of said non-volatile memory and prepares the address translation table in said translation table memory and the write pointer in said pointer memory,” should be inserted after “which,”.
- (6) Page 49, line 10, the expression “a non-volatile memory having” should be inserted after “for managing”.
- (7) Page 49, line 11, the expression “, and each of said memory blocks including physical pages, each of said physical pages including a logical page and a redundancy portion,” should be inserted after “are allocated”.
- (8) Page 49, line 11, the expression “said method comprises the steps of : when said non-volatile memory is activated, reading data from the redundancy portions of said non-volatile memory and preparing” should be inserted after “characterized in that”.
- (9) Page 49, lines 13 and 14, the expression “is stored (S105),” should be deleted.
- (10) Page 49, line 13, the expression “and a write pointer indicating a physical address of an empty page, receiving to-be-written data and logical address;” should be inserted after “of said pages”.
- (11) Page 49, lines 15 and 16, the expression “an empty page in a data storable state is specified from among said pages and a write pointer indicating a physical address of said specified empty page is stored (S107),” should be deleted”.
- (12) Page 49, line 18, the word “ writing” should be inserted after “are supplied,”.
- (13) Page 49, line 19, the expression “ is written” should be deleted.

(14) Page 49, line 25, the expression “ it is discriminated” should be amended as “discriminating” .

(15) Page 49, line 26, the word “ transferring” should be inserted after “memory blocks,”.

5 (16) Page 49, line 27, the expression “is transferred” should be deleted.

(17) Page 49, line 27, the word “erasing” should be inserted after “blocks, and”.

(18) Page 50, line 1, the expression “ is erased” should be deleted.

10 (19) Page 50, lines 2 and 3, the expression “ it is discriminated” should be amended as “discriminating” .

(20) Page 50, line 8, the word “ designating” should be inserted after “condition,,”.

(21) Page 50, line 8, the expression “is designated” should be deleted.

15 6. List of Attached Documents

Replacement sheets of pages 42/1, 42/2, 49/1, 49/2, 50/1 and 50/2 of the claims.